

## **AMENDMENTS TO THE CLAIMS**

1. (Currently Amended) A method comprising:  
~~determining identifying a resource in a computer system to that is capable of generate generating an interrupt; [and]~~  
~~assigning an address range to the resource; the address range to generate an interrupt when accessed for each resource in the set of resources by an operating system for the computer system;~~  
~~configuring the resource to access the address range; and~~  
~~generating the interrupt if the address range is accessed.~~
2. (Currently Amended) The method of claim 1, wherein the address range[s] ~~are-is~~ an input output address range[s].
3. (Currently Amended) The method of claim 1, further comprising:  
correlating an advanced configuration and power interface source language code method with ~~[an]~~ the address range.
4. (Currently Amended) The method of claim 1, wherein the address range[s] includes ~~a~~ system memory address range[s].
5. (Original) The method of claim 1, further comprising:  
correlating a system control interrupt with an advanced configuration and power interface source language code method.
6. (Currently Amended) The method of claim 1, further comprising:  
registering a device driver ~~with-an~~ for the address range by the operating system.
7. (Original) A method comprising:  
receiving an interrupt from an address access request;  
determining the source of the interrupt based on the address access request; and  
invoking an advanced configuration and power interface source language (ASL) code assigned to the address access request.

8. (Currently Amended) The method of claim 67, further comprising:  
notifying a source of the address access request that the ASL code completed.
9. (Currently Amended) The method of claim 67, wherein the address access request  
is an input output address request.
10. (Currently Amended) The method of claim 67, wherein the address access request  
is a system memory address request.
11. (Currently Amended) A device comprising:  
means for determining a resource in a computer system that requires an interrupt; [and]  
means for configuring the resource to access the address range; and  
means for correlating an address range with the resource to generate the interrupt when  
an access request for the address range is generated in the computer system.
12. (Original) The device of claim 11, wherein the address range comprises one of an  
input output address range and a system memory address range.
13. (Original) The device of claim 11, further comprising:  
means for correlating an ASL code segment with the address range to handle the interrupt  
generated by the resource.
14. (Original) A device comprising:  
an advanced configuration and power interface source language (ASL) code segment to  
handle a request of a resource;  
an address protection module to manage the protection of an address space; and  
an operating system level interrupt handler module to receive an interrupt when the  
address protection module detects an address space access and to invoke the ASL code segment  
corresponding to the address space access.
15. (Original) The device of claim 14, wherein the address protection module is an input  
output protection module that generates a general protection fault.

16. (Original) The device of claim 14, wherein the address protection module is a memory protection module that generates a page fault.
17. (Original) A system comprising:  
a processor;  
a memory device coupled to processor;  
an advanced configuration and power interface (ACPI) module to manage power management resources; and  
an operating system module executed by the processor to register a device driver to manage a system resource, the operating system module invoking the ACPI module when a memory access is received that corresponds to an address range registered by the device driver.
18. (Original) The system of claim 17, wherein the address range is an input output address range.
19. (Original) The system of claim 17, wherein the address range is a system memory address range.
20. (Original) A machine readable medium having instructions stored therein which when executed cause a machine to perform a set of operations comprising:  
generating an interrupt based on an address access request corresponding to a predefined range;  
determining the source of the interrupt based on the address access request; and  
invoking an advanced configuration and power interface source language code assigned to the address access request.
21. (Original) The machine readable medium of claim 20,  
notifying a source of the address access request that the ASL code completed.
22. (Original) The method of claim 20, wherein the address access request is an input output address request.
23. (Original) The method of claim 20, wherein the address access request is a system memory address request.